

The listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Currently Amended) A semiconductor device comprising:  
a modulation circuit;  
a demodulation circuit;  
a logic circuit connected to the demodulation circuit;  
an antenna circuit which is electrically connected to the modulation circuit and the demodulation circuit;  
a memory circuit for storing an output signal of the logic circuit; and  
a control circuit configured to limit data writing of the memory circuit so that data is written to the memory circuit only once,  
wherein the memory circuit comprises a ferroelectric capacitor; ~~and~~  
~~wherein data is written to the memory circuit based on the instruction of the control circuit.~~

2. (Currently Amended) A semiconductor device comprising:  
a modulation circuit;  
a demodulation circuit;  
a logic circuit connected to the demodulation circuit;  
an antenna circuit which is electrically connected to the modulation circuit and the demodulation circuit;  
a memory circuit for storing an output signal of the logic circuit; and  
means for controlling ~~data to be written or not to the memory circuit~~ data writing of the memory circuit so that data is written to the memory circuit only once,  
wherein the memory circuit comprises a ferroelectric capacitor; ~~and~~

~~wherein data is written to the memory circuit based on the instruction of the means for controlling data.~~

3. (Original) The semiconductor device according to claim 1 or 2,  
wherein a memory cell that forms the memory circuit includes two transistors  
and two ferroelectric capacitors.

4. (Original) The semiconductor device according to claim 1 or 2,  
wherein a memory cell that forms the memory circuit includes one transistor and  
one ferroelectric capacitor.

5. (Original) The semiconductor device according to claim 1 or 2,  
wherein at least one of the modulation circuit, the demodulation circuit, the logic  
circuit, and the memory circuit is constituted by a thin film transistor.

6. (Original) The semiconductor device according to claim 1 or 2,  
wherein the antenna circuit, the modulation circuit, the demodulation circuit, the  
logic circuit, and the memory circuit are provided over the same insulating substrate.

7. (Original) The semiconductor device according to claim 1 or 2,  
wherein the modulation circuit, the demodulation circuit, the logic circuit, and the  
memory circuit are integrally formed over the same insulating substrate, and  
wherein the antenna circuit is provided over another insulating substrate.

8. (Currently Amended) The semiconductor device according to claim ~~4 or 2~~ 6,  
wherein the insulating substrate is a glass substrate.

9. (Currently Amended) The semiconductor device according to claim ~~4 or 2~~ 6,

wherein the insulating substrate is a plastic substrate.

10. (Currently Amended) The semiconductor device according to claim ~~1~~ or ~~2~~  
6,

wherein the insulating substrate is an insulator in a film form.

11. (Original) The semiconductor device according to claim 1 or 2,  
wherein the antenna circuit is provided over at least one of the modulation  
circuit, the demodulation circuit, the logic circuit, and the memory circuit.

12. (Original) The semiconductor device according to claim 1 or 2,  
wherein a signal inputted to the antenna circuit is a wireless signal.

13. (Original) A product having the semiconductor device according to claim 1  
or 2, wherein the product is selected from the group consisting of a wireless chip, an IC  
card, an IC tag, a transponder, a bill, securities, a passport, a bag, and a garment.

14. (Original) A product having the semiconductor device according to claim 1  
or 2, wherein the product is an electronic apparatus.

15.-23. (Canceled)